Schet No.: 57454-954 PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277

Tsukasa OOISHI, et al. : Confirmation Number: 4782

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Serial No.: 10/607,259 : Group Art Unit: 2811

: Examiner: To be Assigned

:

For: SEMICONDUCTOR DEVICE WITH REDUCED CURRENT CONSUMPTION IN STANDBY

STATE

Filed: June 27, 2003

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop IDS Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This Information Disclosure Statement and 1449 Form is supplemental to that filed June 13, 2003. The reason for this supplemental Information Disclosure Statement and 1449 Form is to correct a typographical error made on the initial 1449 Form from the parent application Serial No. 10/167,437. The country code of the sixth document is incorrectly described. The correct country code is **EP** 632463, not JP as initially filed. Attached are the copies for your reference.

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

Stephen A. Becker Registration No. 26,527

600 13th Street, N.W. Washington, DC 20005-3096 (202) 756-8000 SAB:mcw Facsimile: (202) 756-8087

Date: November 13, 2003

SHEET 1 OF 1

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

ATTY. DOCKET NO. **57454-954**

SERIAL NO.
Continuation of Appl. No.
10/167,437

APPLICANT

OOISHI, TSUKASA, et al.

(PTO-1449)

FILING DATE
June 27, 2003

GROUP

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EXAMINER'S INITIALS	CITE NO.	Include journal publish	name of the author (in serial, symposium, cata	CAPITAL LETTERS	S), title of the article (when approp ge(s), volume-issue number(s), p	oriate), title of the ite	em (book country	k, magazine where	•
l		EXAN	MINER			DATE CONSIDERE	 ≣D		

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¹ Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.